UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,015,559 B2

Page 1 of 1

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INVENTOR(S)

: Tim Murphy and Lee Gotcher

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column, Line	<u>Reads</u>	Should Read
Column 5, Lines 16-17	"electromagnetic signals	electromagnetic
and 19	107,105,104"	signals 107, 105, 104
Column 5, Line 52	"signals 131, and"	signals 131 and
Column 6, Lines 34-36	"a memory-cell array	a memory cell array
	coupled to the address	coupled to the address
	decoder, control circuit,	decoder, control
	and read/write circuit, the	circuit, and read/write
	memory-cell array; and"	circuit; and
Column 7, Line 2	"converter mounted on	converter is mounted
	the chip"	on the chip

Signed and Sealed this

Third Day of February, 2009

JOHN DOLL Acting Director of the United States Patent and Trademark Office